			Application Number	10/694,919
TRANSMITTAL FORM (to be used for all correspondence after initial filing)			Filing Date	October 27, 2003
			First Named Inventor	Hwang, Yean-Yow
			Art Unit	To Be Assigned
			Examiner Name	To Be Assigned
Total Number of Page Submission	es in This	5	Attorney Docket Number	015114-066600US
		ENC	LOSURES (Check all that	apply)
Fee Transmittal Form		☐ Drawing(s)		After Allowance Communication to
Fee Attached		Licens	ing-related Papers	. Appeal Communication to Board of and Interferences
Amendment/Reply		Petition		Appeal Communication to Group Notice, Brief, Reply Brief)
After Final			n to Convert to a lonal Application	Proprietary Information
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			F APPLICANT, ATTOR	IEY, OR AGENT
Firm Townsend and Townsend and Crew LLP or Steven Cahill Reg. No. 44,578				
Individual	Steven Carilli		Ne	y. 140. 44,576
Signature	Stan	en C	ahill	
Date April <u>lle</u> , 2004				,
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				ed with the United States Postal Service with sufficie andria, VA 22313-1450 on the date shown below.
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Attorney Docket No.: 015114-066600US Client Reference No.: A1052

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

on APRIL 16, 2004

TOWNSEND and TOWNSEND and CREW LLP

Ву:__

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Yean-Yow Hwang et al.

Application No.: 10/694,919

Filed: October 27, 2003

For: TECHNIQUES FOR MAPPING FUNCTIONS TO LOOKUP TABLES ON PROGRAMMABLE CIRCUITS

Examiner: To Be Assigned

Art Unit: To Be Assigned

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

Yean-Yow Hwang et al. Application No.: 10/694,919

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representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that <u>no fee is required</u> for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Steven Cahill Reg. No. 44,578

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Tel: 650-326-2400 Fax: 650-326-2422

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/694,919 Filing Date October 27, 2003 First Named Inventor Hwang, Yean-Yow Art Unit To Be Assigned Examiner Name To Be Assigned Attomey Docket Number 015114-066600US

(use as many sheets as necessary)

NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item Cite xaminer (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), T 2 No.1 Initials * publisher, city and/or country where published. Cong, J and ping, Y (1996). "Combinational logic synthesis for (LUT) based field programmable gate arrays," ACM 0001 Trans. Des. Autom. Electron. Syst. 1(2):145-204, Cong, J. and Hwang, Y. (2001). "Boolean Matching for LUT-Based Logic Blocks With Applications to Architecture Evaluation and Technology Mapping," IEEE Transactions on Computer-Aided Design of Integrated Circuits and 0002 Systems, 20(9):1077-1090.

Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.